FIG. 1

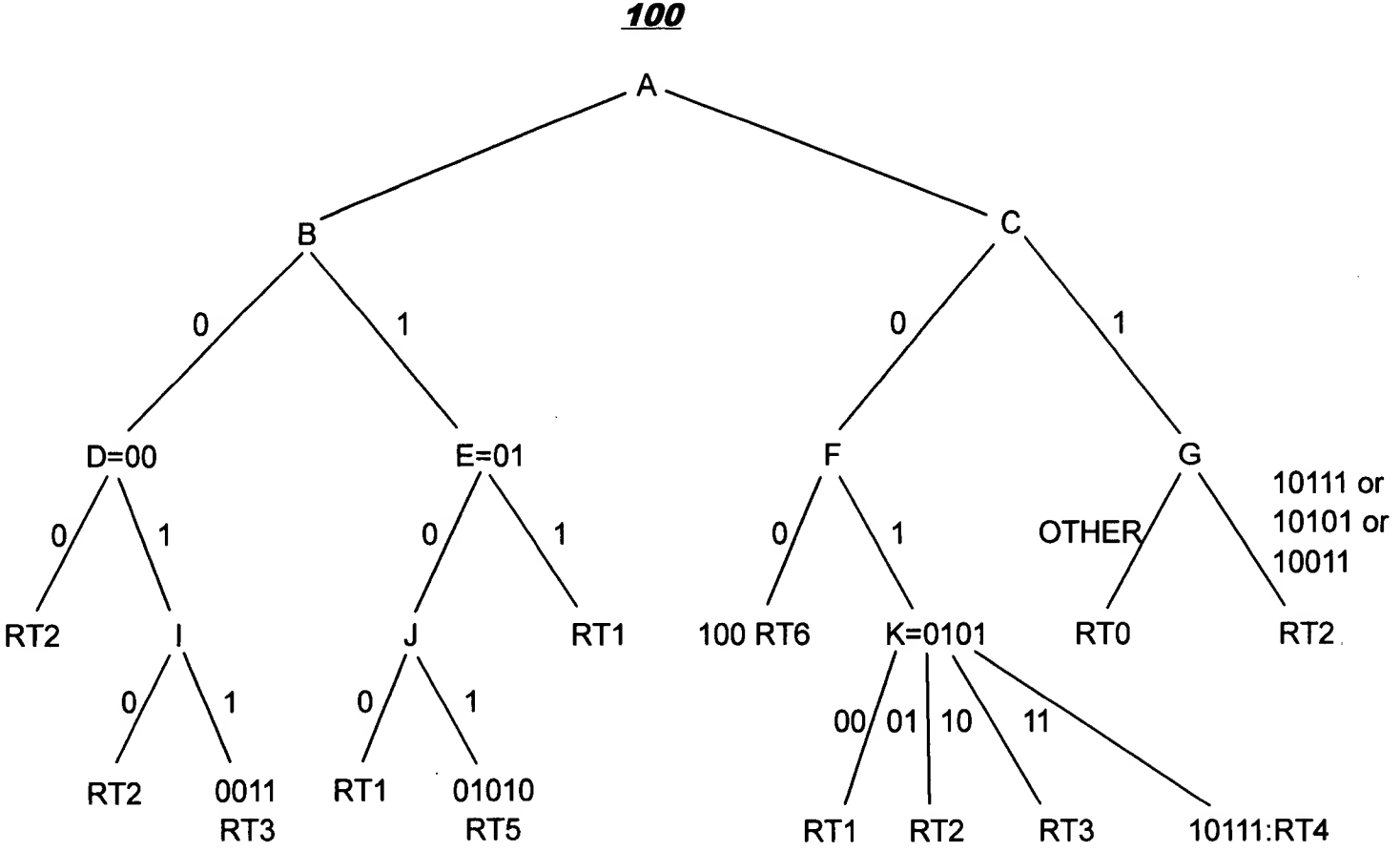

100
A
B
C
0
1
0
1
D=00
E=01
F
G
0
1
0
1
0
1
OTHER
10111 or
10101 or
10011
RT2
I
J
RT1
100 RT6
K=0101
RT0
RT2
0
1
0
1
00
01
10
11
RT2
0011
RT3
RT1
01010
RT5
RT1
RT2
RT3
10111:RT4

FIG. 2

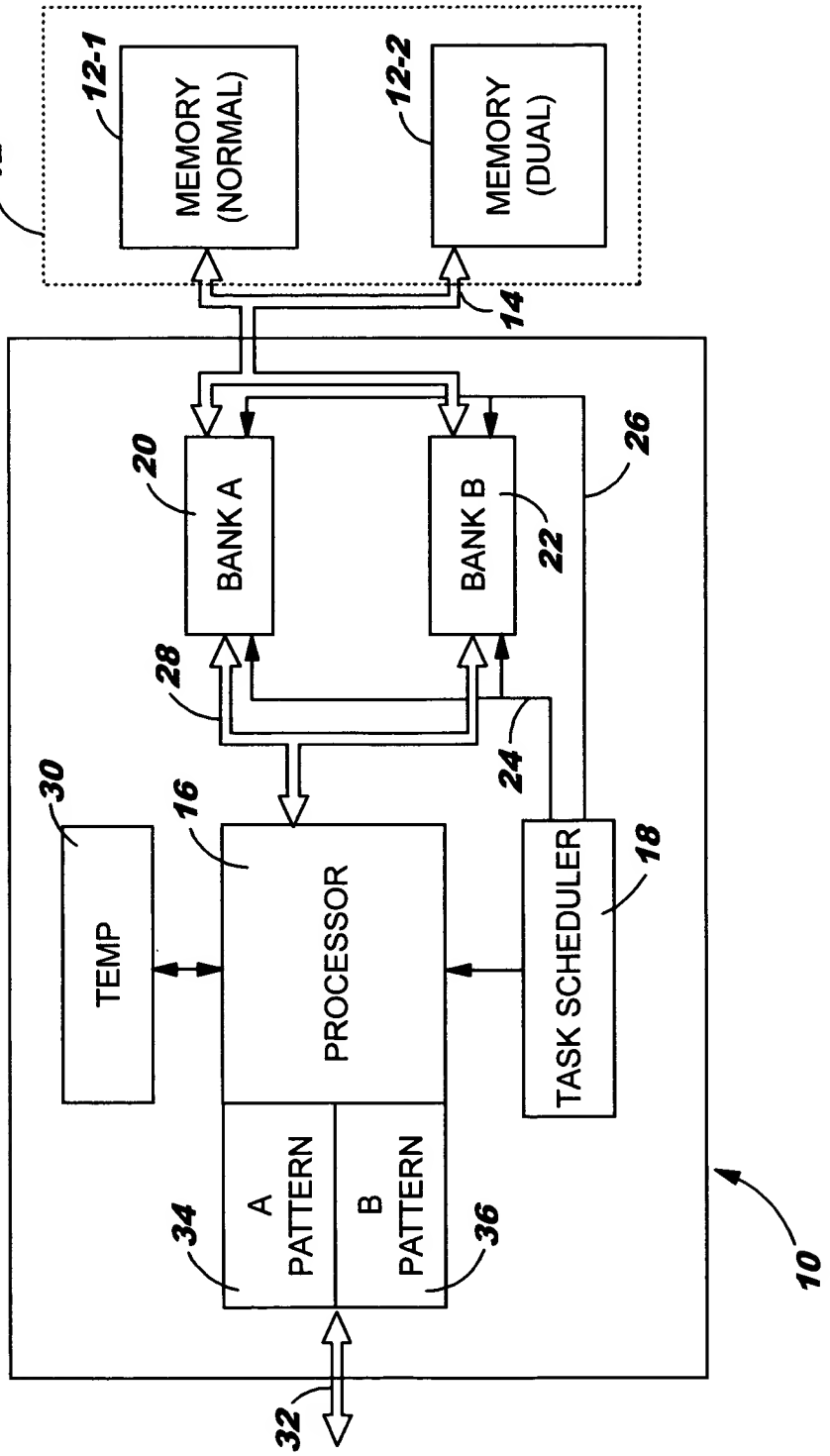

200
12
12-1
MEMORY (NORMAL)
12-2
MEMORY (DUAL)
14
20
BANK A
22
BANK B
26
28
24
30
TEMP
16
PROCESSOR
18
TASK SCHEDULER
34
A PATTERN
B PATTERN
36
32
10

FIG. 3

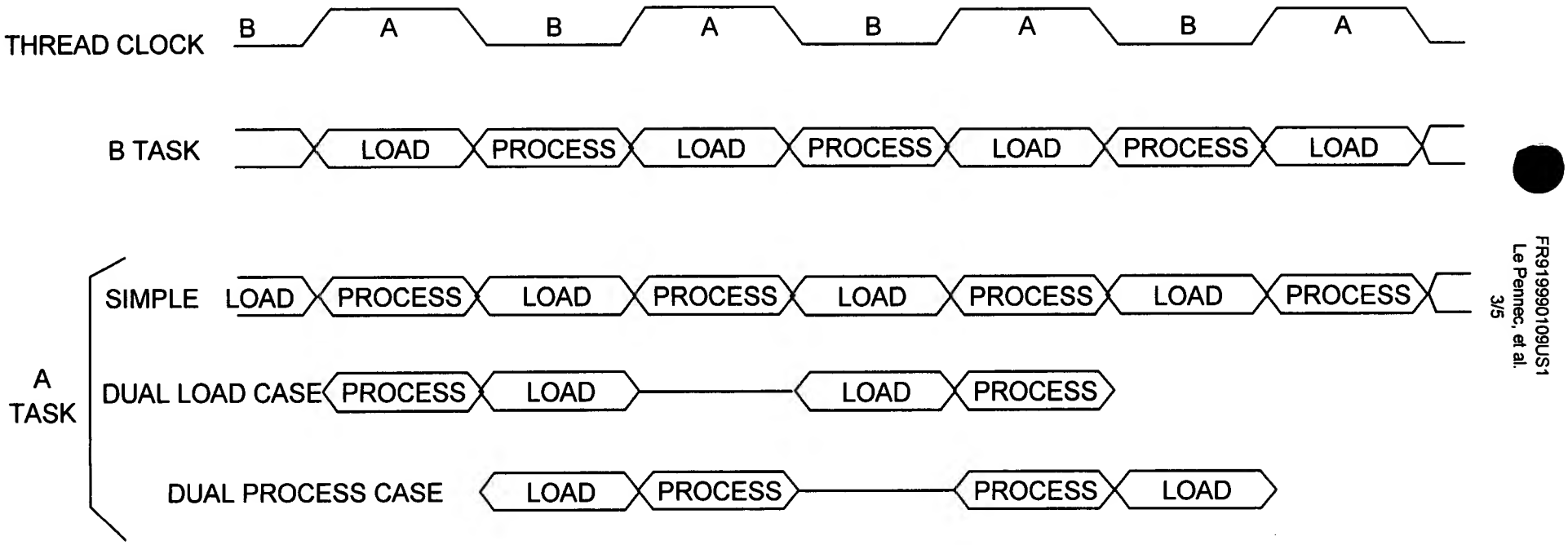

THREAD CLOCK
B
A
B
A
B
A
B
A
B TASK
LOAD
PROCESS
LOAD
PROCESS
LOAD
PROCESS
LOAD
A TASK
SIMPLE
LOAD
PROCESS
LOAD
PROCESS
LOAD
PROCESS
LOAD
PROCESS
DUAL LOAD CASE
PROCESS
LOAD
LOAD
PROCESS
DUAL PROCESS CASE
LOAD
PROCESS
PROCESS
LOAD

FIG. 4

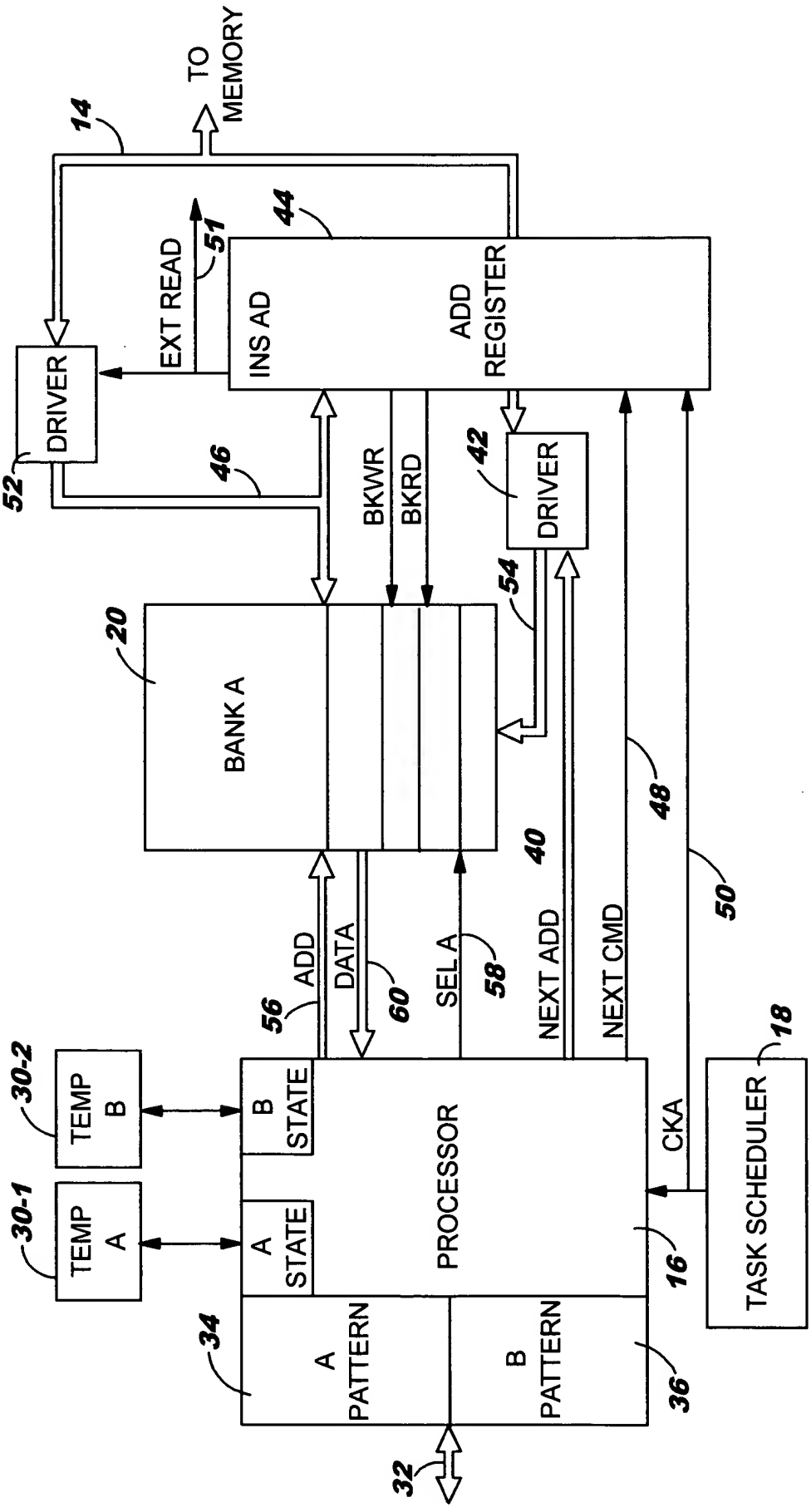
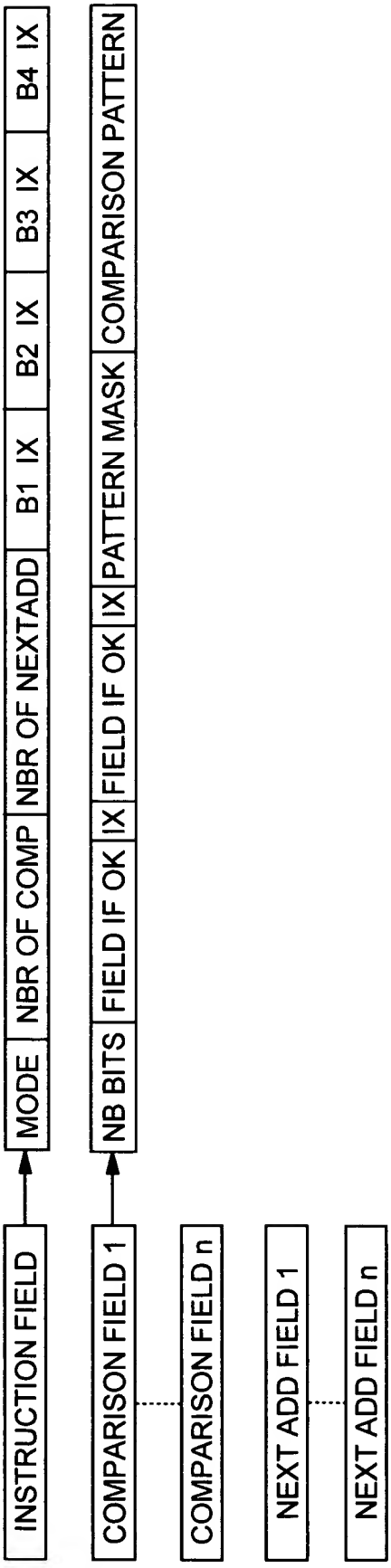

200
TO MEMORY
14
EXT READ
51
44
INS AD
ADD REGISTER
DRIVER
52
46
BKWR
BKRD
42
DRIVER
54
20
BANK A
48
40
50
ADD
DATA
SEL A
NEXT ADD
NEXT CMD
56
60
58
30-2
TEMP B
B STATE
30-1
TEMP A
A STATE
PROCESSOR
CKA
18
TASK SCHEDULER
16
34
A PATTERN
B PATTERN
36
32

FIG. 4

FIG. 5


INSTRUCTION FIELD
MODE
NBR OF COMP
NBR OF NEXTADD
B1 IX
B2 IX
B3 IX
B4 IX
COMPARISON FIELD 1
NB BITS
FIELD IF OK
IX
FIELD IF OK
IX
PATTERN MASK
COMPARISON PATTERN
COMPARISON FIELD n
NEXT ADD FIELD 1
NEXT ADD FIELD n